



Exemplar
Global
iNARTE

EMC DESIGN ENGINEER & SENIOR EMC DESIGN ENGINEER CRITERIA AND INSTRUCTIONS

EMC DESIGN ENGINEER CERTIFICATION CRITERIA

1. Complete the online Application and submit the **non-refundable** examination fee for the requested examination.
2. Submission of the application form implies agreement to adhere to the iNARTE Code of Ethics.
3. Provide specific record of nine years or more of experience in engineering work. Provide an up-to-date resume or complete the EMC Detailed Work History Form
 - a. Graduation from an iNARTE-approved engineering curriculum of four years shall be considered equivalent to four years of such required experience.
 - b. Graduation in a physical science curriculum other than engineering will be evaluated by iNARTE.
 - c. Graduation from a college with a BSET in Engineering Technology (BSET) shall be considered as equivalent to two years of such required experience.
 - d. Graduation in a curriculum other than engineering or physical science will be evaluated by iNARTE.
 - e. The completion of five (5) years of EMC design work experience shall be considered as sufficient work experience without regard to educational achievements.
 - f. Undergraduates may attempt the examination and if successful will be awarded an Associate Certification pending final graduation.
4. Provide evidence of education and training. Official school transcripts are required. Photocopies of applicable training certificates may be submitted.
5. References: Using the iNARTE reference forms (Pages 10-11), submit a minimum of 1 academic, (faculty member), reference or 1 supervisory reference supporting character and competency as an EMC Design Engineer. Reference forms must be signed and forwarded directly to iNARTE.
6. Pass the EMC Design Engineer examination.
7. The examination is significantly different from the established iNARTE EMC Certification Exam in the following respects: The exam questions are all related to EMC Design for Compliance and other EMC fundamentals. There are few if any questions related to Field Theory, EMC Emission or Susceptibility standards and disciplines better suited to Test, Measurement and Mitigating



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Engineering.

| Category | EMC Design Engineer Level | Senior EMC Design Engineer Level |
|--|----------------------------------|---|
| EMC Countermeasures & Components | Application | Expertise |
| EMC Design & Design Review | Expertise | Expertise |
| EMC Simulation & Rule Check | Application | Expertise |
| Signal Integrity & Power Integrity | Application/Basic | Expertise |
| Electronics Circuits & Power Electronics | Basic | Expertise/Application |
| Basic EMC Knowledge | Expertise | Expertise |
| Terminology | Expertise | Expertise |
| Mathematics | Basic | Basic |
| Electromagnetics & Shielding | Application | Expertise |
| Electrical Circuit Theory | Application | Expertise |
| Measurement & Analysis | Basic | Application |
| Specifications and Standards | Basic | Basic |

SENIOR EMC DESIGN ENGINEER CERTIFICATION CRITERIA

1. Complete the online Application and submit the non-refundable examination fee for the requested examination.
2. Submission of the application form implies agreement to adhere to the iNARTE Code of Ethics. Available at: <http://www.narte.org/h/codeofethics.asp>.
3. Provide a specific record of education and electronic design work experience employing EMC principles. Provide an up-to-date resume.
 - a. Completion of a minimum of three (3) years practical design engineering work after having been certified as an iNARTE/KEC EMC Design Engineer.
 - b. Graduation from an iNARTE-approved bachelor degree engineering curriculum, (e.g. BSEE), followed by four (4) years practical design engineering work experience.
 - c. Graduation in a physical science curriculum other than engineering, followed by four (4) years practical design engineering work experience will be evaluated by



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- d. Teaching: EMC design engineering teaching of a character satisfactory to iNARTE may be considered as a maximum of two years' experience.
4. Provide evidence of education and training. Official school transcripts are required. Photocopies of applicable training certificates may be submitted.
5. References: Using the iNARTE reference forms (Pages 10-11), submit a minimum of 1 supervisory reference and 2 additional peer references each supporting character and competency as an EMC Design Engineer. Reference forms must be signed and forwarded directly to iNARTE.
6. Pass the Senior EMC Design Engineer examination.(See Above information on EMC-DE Exam)